**Register changes made to ObiWan from ElsaOlaf\_ES5**

The following are the changes made to the register on Wan. Changes are compared to the ElsaOlaf\_ES5 version.

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| **Register address, Bit #** | **Function on ElsaOlaf** | **Function on ObiWan** | **Notes** |
| 0x0c, <7> | Unused |  |  |
| 0x0c, <4> | Unused | *PreAmpMode<2>*- Internal use by MMC to change ADC amplifier gain and achieve a 150mV range on ADC Isense input. | This is not to be provided for external customer use. To be used only by MMC. |
| 0x0d, <7:0> | Unused | Internal use by MMC to store inductor DCR. | Register was a test typ, now is customer type. |
| 0x16, <2> | Was *PreAmpMode<2>* | Moved to customer area, see above. |  |
| 0x32, <3> | Unused | *Vx\_uv\_sel*- Provides and option to program VX undervoltage threshold to either 100mv (when low) or 200mV (when high). | Was fixed at 100mV on ElsaOlaf. |
| 0x32, <2> | Unused | *global\_olaf\_fault*- When ‘1’, a fault on one Obi causes both Obis to shutdown.  When ‘0’, only the Obi which has a fault shuts down, the second Obi continues operating. | Valid only when dual Olaf app with independent Vouts is configured. |
| 0x52, <7:6> | Unused | *Vout\_uv\_ov<1:0>*- These bits indicate when vout is in an UV fault. |  |
| 0x52, <5:4> | Unused | *Buck\_short<1>*- These bits indicate when vout is in a short fault. |  |
| 0x52, <3:2> | Unused | *Buck\_ocp<1:0>*- These bits indicate when the secondary ilim fault has been triggered. |  |
| 0x52, <1:0> | Unused | *Olaf\_ot<1:0>*- These bits indicate when an over temperature fault has been triggered on an olaf. |  |
| 0x53, <4> | *buckduty\_flt*- removed | Unused |  |
| 0x53, <3> | *Cmpclamp\_flt*- removed | Unused |  |
| 0x53, <2> | *Vxlo\_flt*- removed | Unused |  |
| 0x6d, <7> | Unused | *tr\_listen*- When this bit is ‘0’, the internal power good comparators are enabled immediately after soft start finishes. If ‘1’, there is a fixed 30µs delay after soft start timeout before the power good comparators are enabled. |  |